One week Short Term Training Program on “FPGA based Digital System Design” (ISTE approved) is going to be held in Electronics Engineering Department during 14/11/2016 to 19/11/2016.

Contents of STTP:

- Current trends and role of programmable logic devices in VLSI Design Industry
- Front end design using Xilinx Vivado software
- Simulation, Synthesis & Implementation
- Reconfiguration of FPGA
- ASIC & FPGA Flow
- Hardware and software co-simulation
- Verilog concepts and system verilog
- Synthesis and FPGA Implementation
- Reconfigurable computing and its lab
- Soft core processor design

Registration Fees: Rs. 2500/

Resource persons: Eminent faculties from institutes of high repute and Industry.

Eligibility: Faculties from Engineering colleges, Polytechnics and working professionals from industry