



Shri Ramdeobaba College of Engineering and Management, Nagpur
Katol Road, Nagpur, Maharashtra, India - 440 013.

Examination :END SEMESTER REGULAR EXAMINATION SUMMER 2020 EX (RS 2020 EX)
Department : RCOEM - M.Tech.(Very Large Scale Integration Design) - M.Tech VLSI Design
Semester : II

Sr.No.	SeatNo	Mail Id	mobile No	Student Name	Analog IC Design(ENT556)	System Verilog for Verification(ENT557)	Research Methodology(ENT558)	VLSI Signal Processing(ENT559-1)	MEMS(ENT559-3)	VLSI Design Automation(ENT560)
1	ENV-20S- 2301	pandess_3@rknec.edu	9766285805	Sonali Suraj Pande	Y	Y	Y	Y	Y	Y