

**SHRI RAMDEOBABA COLLEGE OF ENGINEERING AND MANAGEMENT, NAGPUR.**  
**(An Autonomous College of Rashtrasant Tukadoji Maharaj Nagpur University, Nagpur)**

**GAZETTE : END SEMESTER EXAMINATION MS-2017**

Examination: **Fifth Semester(Ex-Students)**

Department: Information Technology

Programme: **B.E.(Information Technology)**

SR. NO.	ROLL NO.	NAME OF STUDENT	FATHER'S NAME	MOTHER'S NAME	GEN DER	ITT 301	ITP 301	ECT 311	ITT 302	ITT 303	ITT 304	ITP 304	SGPA	CGPA	RESULT
1	ITU17S-5901	NITIN KUMAR SHARMA	PRADEEP	MANJU LATA	M	CD	BC	BC	CD	CD	CD	BC	5.47	5.09	Successful
2	ITU17S-5902	YUNIK BHAGAVATIPRASAD BAJORIA	BHAGAVATIPRASAD	RAJANI	M	CC	CC	BC	BC	FF	CD	BB	5.09	5.73	Unsuccessful
3	ITU17S-5903	KERAN DESHMUKH	ANIL	ARCHANA	M	I	CC	I	I	FF	I	BB	0.60	4.32	Unsuccessful

ITT301 - MICROPROCESSOR & INTERFACING

ITP301 - MICROPROCESSOR & INTERFACING

ECT311 - DIGITAL COMMUNICATION

ITT302 - SYSTEM SOFTWARE

ITT303 - THEORY OF COMPUTATION

ITT304 - SOFTWARE ENGINEERING

ITP304 - SOFTWARE ENGINEERING

AA=10, AB=9, BB=8, BC=7, CC=6, CD=5, FF=0, I=Absent, Z=Detained.

By the order of Examination Committee.



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**GAZETTE : END SEMESTER EXAMINATION MS-2017**

Examination: **Fifth Semester(Ex-Students)**

Department: **Computer Science & Engineering**

Programme: **B.E.(Computer Science & Engineering)**

SR. NO.	ROLL NO.	NAME OF STUDENT	FATHER'S NAME	MOTHER'S NAME	GEN DER	CST 301	CST 302	CSP 302	CST 303	CSP 303	CST 304	CST 305	CSP 305	CSP 306	SGPA	CGPA	RESULT
1	CSU17S-5901	ADITYA ANNASAHEB DAHALE	ANNASAHEB	LATA	M	I	I	CC	I	CC	CD	I	BC	BB	2.11	3.77	Unsuccessful

CST301 - SYSTEM PROGRAMMING

CST303 - SOFTWARE ENGINEERING

CST305 - COMPUTER NETWORKS

CST302 - COMPUTER GRAPHICS & GUI DESIGN TECHNOLOGIES

CSP303 - SOFTWARE ENGINEERING LAB

CSP305 - COMPUTER NETWORKS LAB

CSP302 - COMPUTER GRAPHICS & GUI DESIGN TECHNOLOGIES LAB

CSP306 - SOFTWARE TECH. LAB-I LAB

By the order of Examination Committee.



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**GAZETTE : END SEMESTER EXAMINATION MS-2017**

Examination: **Fifth Semester(Ex-Students)**

Programme: **B.E.(Electronics & Communication Engineering)**

SR. NO.	ROLL NO.	NAME OF STUDENT	MOTHER'S NAME	GEN DER	HUT 301	EET 314	ECT 301	ECP 301	ECT 302	ECP 302	ECT 303	ECP 304	SGPA	CGPA	RESULT
1	ECU17S-5901	ESHAN SINGH	ABHA	M	CC	BC	I	CC	CD	CC	CD	SF	4.87	4.75	Unsuccessful

HUT301 - PRINCIPLES OF ECONOMICS & MANAGEMENT

ECT302 - MICROPROCESSOR & INTERFACING

EET314 - CONTROL SYSTEM ENGINEERING

ECP302 - MICROPROCESSOR & INTERFACING LAB

ECT301 - FUNDAMENTALS OF COMMUNICATION ENGINEERING

ECT303 - COMPUTER ORGANIZATION

ECP301 - FUNDAMENTALS OF COMMUNICATION ENGINEERING LAB

ECP304 - SYSTEM SIMULATION

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**GAZETTE: END SEMESTER EXAMINATION MS-2017**

Examination: **Fifth Semester(Ex-Students)**

Department: **Electronics Design Technology**

Programme: **B.E.(Electronics Design Technology)**

SR. NO.	ROLL NO.	STUDENT ID.	NAME OF STUDENT	FATHER'S NAME	MOTHER'S NAME	GEN DER	HUT 301	EDT 301	EDP 301	EDT 302	EDT 303	EDP 303	EDT 304	EDP 304	EDP 305	SGPA	CGPA	RESULT
1	EDU17S-5901	BE10EDU406	SAGAR BHOYAR	PRALHAD	KALPANA	M	BC	CC	I	I	BC	I	BB	AA	SF	5.47	1.11	Unsuccessful

HUT301 - PRINCIPLES OF ECONOMICS & MANAGEMENT

EDT302 - STATISTICAL SIGNAL ANALYSIS

EDT304 - MICROPROCESSOR & INTERFACING

EDT301 - DIGITAL SYSTEMS DESIGN WITH HDL

EDT303 - PCB TECHNOLOGY

EDP304 - MICROPROCESSOR & INTERFACING

EDP301 - DIGITAL SYSTEMS DESIGN WITH HDL

EDP303 - PCB TECHNOLOGY

EDP305 - CIRCUIT SIMULATION-I

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**GAZETTE : END SEMESTER EXAMINATION MS-2017**

Examination: **Fifth Semester(EX-STUDENT)**

Department: Electronics Engineering

Programme: **B.E. (Electronics Engineering)**

SR. NO.	ROLL NO.	NAME OF STUDENT	FATHER'S NAME	MOTHER'S NAME	GEN DER	HUT 301	ENT 301	ENP 301	ENT 302	ENP 302	ENT 303	ENT 304	ENP 304	ENP 305	SGPA	CGPA	RESULT
1	ENU17S-5901	PREETESH HARISHANKAR YADAV	HARISHANKAR	NIRMALADEVI	M	CD	CD	CC	CC	BC	CD	CC	CC	USF	5.58	1.13	Unsuccessful
2	ENU17S-5902	SNEH DILIP DIXIT	DILIP	UMA	M	CD	CD	CC	FF	BC	FF	CD	BC	SF	3.44	0.70	Unsuccessful

HUT301 - PRINCIPLES OF ECONOMICS & MANAGEMENT

ENT302 - STATISTICAL SIGNAL ANALYSIS

ENT304 - MICROPROCESSOR & INTERFACING

ENT301 - DIGITAL SYSTEMS DESIGN WITH HDL

ENP302 - STATISTICAL SIGNAL ANALYSIS LAB

ENP304 - MICROPROCESSOR & INTERFACING LAB

ENP301 - DIGITAL SYSTEMS DESIGN WITH HDL

ENT303 - FIELD & RADIATING SYTEM

ENP305 - CIRCUIT SIMULATION LAB

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