

Programme Name : RCOEM - M.Tech.(Very Large Scale Integration Design) - M.Tech VLSI Design
Exam Name : END SEMESTER REGULAR EXAMINATION WINTER 2020 (RW 2020)(M.Tech.(Very Large Scale Integration Design))
Syllabus Pattern : 2016
Classes / Semester : I
Report Genrate Date : May 08, 2021

Sr. No.	Seat No.	Student Unique No.	Student Name	Email	Mobile	Course Code	Course Name
1	ENV20W-1004	20ENV1002ENV1	Mahima Vimal Kishore Singh Rana	ranamk@rk nec.edu	9822696101	ENT551	CMOS Digital Circuit Design
						ENT552	Digital System Design
						ENT553	Embedded System and RTOS
						ENT554	Semiconductor Devices
						ENP551	CMOS Digital Circuit Design Lab
						ENP552	Digital System Design Lab
						ENP553	Embedded System and RTOS Lab
						ENT555-1	VLSI Technology
2	ENV20W-1005	20ENV1003ENV1	Minal Pandey	pandeym_1@rk nec.edu	8767607085	ENT551	CMOS Digital Circuit Design
						ENT552	Digital System Design
						ENT553	Embedded System and RTOS
						ENT554	Semiconductor Devices
						ENP551	CMOS Digital Circuit Design Lab
						ENP552	Digital System Design Lab
						ENP553	Embedded System and RTOS Lab
						ENT555-1	VLSI Technology
3	ENV20W-1006	20ENV1004ENV1	Pournima Nitin Joshi	joshipn@rk nec.edu	7020013563	ENT551	CMOS Digital Circuit Design
						ENT552	Digital System Design
						ENT553	Embedded System and RTOS
						ENT554	Semiconductor Devices
						ENP551	CMOS Digital Circuit Design Lab
						ENP552	Digital System Design Lab
						ENP553	Embedded System and RTOS Lab
						ENT555-1	VLSI Technology
4	ENV20W-1002	20ENV1007ENV1	Shivam Shankar Tekode	tekodess@rk nec.edu	9326656511	ENT551	CMOS Digital Circuit Design
						ENT552	Digital System Design
						ENT553	Embedded System and RTOS
						ENT554	Semiconductor Devices
						ENP551	CMOS Digital Circuit Design Lab
						ENP552	Digital System Design Lab
						ENP553	Embedded System and RTOS Lab
						ENT555-1	VLSI Technology
5	ENV20W-1003	20ENV1001ENV1	Karishma Sanjay Nakhate	nakhateks@rk nec.edu	7756966588	ENT551	CMOS Digital Circuit Design
						ENT552	Digital System Design
						ENT553	Embedded System and RTOS
						ENT554	Semiconductor Devices
						ENP551	CMOS Digital Circuit Design Lab
						ENP552	Digital System Design Lab
						ENP553	Embedded System and RTOS Lab
						ENT555-1	VLSI Technology

6	ENV20W-1001	20ENV1006ENV1	Manoj Janardhan Pandey	pandeymj_1@rk nec.edu	9766773519	ENT551	CMOS Digital Circuit Design
						ENT552	Digital System Design
						ENT553	Embedded System and RTOS
						ENT554	Semiconductor Devices
						ENP551	CMOS Digital Circuit Design Lab
						ENP552	Digital System Design Lab
						ENP553	Embedded System and RTOS Lab
						ENT555-1	VLSI Technology
7	ENV20W-1007	20ENV1005ENV1	Prachi Sunil Sakhare	sakhareps_1@rk nec.edu	9588453902	ENT551	CMOS Digital Circuit Design
						ENT552	Digital System Design
						ENT553	Embedded System and RTOS
						ENT554	Semiconductor Devices
						ENP551	CMOS Digital Circuit Design Lab
						ENP552	Digital System Design Lab
						ENP553	Embedded System and RTOS Lab
						ENT555-1	VLSI Technology