

**Programme Name : RCOEM - M.Tech.(Very Large Scale Integration Design) - M.Tech VLSI Design**  
**Exam Name : END SEMESTER REGULAR EXAMINATION WINTER 2020 EX (RW 2020 EX)( M.Tech.(Very Large Scale Integration Design) )**  
**Syllabus Pattern : 2016**  
**Classes / Semester : I**  
**Report Genrate Date : May 08, 2021**

| Sr. No. | Form No.    | Student Unique No. | Student Name           | Email                | Mobile     | Course Code | Course Name                        |
|---------|-------------|--------------------|------------------------|----------------------|------------|-------------|------------------------------------|
| 1       | ENV20W-3101 | 17ENV1017          | Sonali Suraj Pande     | pandess_3@rk nec.edu | 9527887165 | ENT552      | Digital System Design              |
|         |             |                    |                        |                      |            | ENT554      | Semiconductor Devices              |
|         |             |                    |                        |                      |            | ENT555-3    | Advanced Digital Signal Processing |
| 2       | ENV20W-3102 | 19ENV1005ENV1      | Monika Khushal Kapgate | kapgatemk@rk nec.edu | 9028794140 | ENT551      | CMOS Digital Circuit Design        |
|         |             |                    |                        |                      |            | ENT553      | Embedded System and RTOS           |
|         |             |                    |                        |                      |            | ENT554      | Semiconductor Devices              |