

Programme Name : RCOEM - M.Tech.(Very Large Scale Integration Design) - M.Tech VLSI Design
Exam Name : END SEMESTER REGULAR EXAMINATION WINTER 2020 (RW 2020) (M.Tech.(Very Large Scale Integration Design))
Syllabus Pattern : 2016
Classes / Semester : III
Report Genrate Date : Nov 20, 2020

Sr. No.	Seat No.	Student Unique No.	Student Name	Batch	Course Code	Course Name
1	ENV20W-3001	34305	Pande Sonali Surajnarayan	Div-1	ENP653	Project Phase I
					ENT651	System-on-Chip
					ENT652-1	VLSI Testing
2	ENV20W-3002	41410	Kaggate Monika Khushal	Div-1	ENP653	Project Phase I
					INC	Incentive
					ENT651	System-on-Chip
					ENT652-1	VLSI Testing
3	ENV20W-3003	41446	Megha Manoj Verma	Div-1	ENP653	Project Phase I
					INC	Incentive
					ENT651	System-on-Chip
					ENT652-1	VLSI Testing
4	ENV20W-3004	41529	Gillurkar Hemant Haridasji	Div-1	ENP653	Project Phase I
					INC	Incentive
					ENT651	System-on-Chip
					ENT652-1	VLSI Testing

5	ENV20W-3005	42460	Krutika	Div-1	ENP653	Project Phase I
					INC	Incentive
					ENT651	System-on-Chip
					ENT652-1	VLSI Testing
6	ENV20W-3006	43179	Padegaonkar Rasika Dineshrao	Div-1	ENP653	Project Phase I
					INC	Incentive
					ENT651	System-on-Chip
					ENT652-1	VLSI Testing
7	ENV20W-3007	43322	Angel Sharadkant Thomas	Div-1	ENP653	Project Phase I
					INC	Incentive
					ENT651	System-on-Chip
					ENT652-1	VLSI Testing
8	ENV20W-3008	43694	Kalyani Gopalrao Ingale	Div-1	ENP653	Project Phase I
					INC	Incentive
					ENT651	System-on-Chip
					ENT652-1	VLSI Testing
9	ENV20W-3009	43757	Pawar Rohit Baburaj	Div-1	ENP653	Project Phase I
					INC	Incentive
					ENT651	System-on-Chip
					ENT652-1	VLSI Testing

10	ENV20W-3010	43893	Sushmit Saantra	Div-1	ENP653	Project Phase I
					INC	Incentive
					ENT651	System-on-Chip
					ENT652-1	VLSI Testing