

**Programme Name : RCOEM - B.E.(Electronics Design Technology) - Electronics Design Technology (EDU)**  
**Exam Name : END SEMESTER REGULAR EXAMINATION WINTER 2020 (RW 2020) ( B.E.(Electronics Design Technology) )**

**Syllabus Pattern : 2011**

**Classes / Semester : VII**

**Report Genrate Date : Nov 20, 2020**

Sr. No.	Seat No.	Student Unique No.	Student Name	Batch	Course Code	Course Name
1	EDU20W-7001	27618	Tiwari Tanishq Manoj	Div-1	EDP402	CMOS VLSI Design Lab
					EDP403	Digital Signal Processing Lab
					EDP406	Project Phase -1
					EDT405-4	MOOC
					EDP407	FPGA Based Implementation
					INC	INCENTIVE
					EDT401	Design Of Electronic Equipments
					EDT402	CMOS VLSI Design
					EDT403	Digital Signal Processing
					EDT404-3	Digital Communication
2	EDU20W-7002	29670	Padole Sakshi Anil	Div-1	EDP402	CMOS VLSI Design Lab
					EDP403	Digital Signal Processing Lab
					EDP406	Project Phase -1
					EDT405-4	MOOC
					EDP407	FPGA Based Implementation
					INC	INCENTIVE
					EDT401	Design Of Electronic Equipments
					EDT402	CMOS VLSI Design
					EDT403	Digital Signal Processing
					EDT404-3	Digital Communication
3	EDU20W-7003	30006	Tokekar Omkar Paresh	Div-1	EDP402	CMOS VLSI Design Lab
					EDP403	Digital Signal Processing Lab
					EDP406	Project Phase -1
					EDT405-4	MOOC
					EDP407	FPGA Based Implementation
					INC	INCENTIVE
					EDT401	Design Of Electronic Equipments
					EDT402	CMOS VLSI Design
					EDT403	Digital Signal Processing
					EDT404-3	Digital Communication

4	EDU20W-7004	30299	Singru Nikita Prashant	Div-1	EDP402	CMOS VLSI Design Lab
					EDP403	Digital Signal Processing Lab
					EDP406	Project Phase -1
					EDT405-4	MOOC
					EDP407	FPGA Based Implementation
					INC	INCENTIVE
					EDT401	Design Of Electronic Equipments
					EDT402	CMOS VLSI Design
					EDT403	Digital Signal Processing
					EDT404-3	Digital Communication
5	EDU20W-7005	30621	Swarn Zargar	Div-1	EDP402	CMOS VLSI Design Lab
					EDP403	Digital Signal Processing Lab
					EDP406	Project Phase -1
					EDT405-4	MOOC
					EDP407	FPGA Based Implementation
					INC	INCENTIVE
					EDT401	Design Of Electronic Equipments
					EDT402	CMOS VLSI Design
					EDT403	Digital Signal Processing
					EDT404-3	Digital Communication
6	EDU20W-7006	30819	Dhanshri Manoj Mundada	Div-1	EDP402	CMOS VLSI Design Lab
					EDP403	Digital Signal Processing Lab
					EDP406	Project Phase -1
					EDT405-4	MOOC
					EDP407	FPGA Based Implementation
					INC	INCENTIVE
					EDT401	Design Of Electronic Equipments
					EDT402	CMOS VLSI Design
					EDT403	Digital Signal Processing
					EDT404-3	Digital Communication
7	EDU20W-7007	31981	Bhumika Jugalkishor Mundada	Div-1	EDP402	CMOS VLSI Design Lab
					EDP403	Digital Signal Processing Lab
					EDP406	Project Phase -1
					EDT405-4	MOOC
					EDP407	FPGA Based Implementation
					INC	INCENTIVE
					EDT401	Design Of Electronic Equipments

					EDT402	CMOS VLSI Design
					EDT403	Digital Signal Processing
					EDT404-3	Digital Communication
8	EDU20W-7008	32744	Riddhi Raut	Div-1	EDP402	CMOS VLSI Design Lab
					EDP403	Digital Signal Processing Lab
					EDP406	Project Phase -1
					EDT405-4	MOOC
					EDP407	FPGA Based Implementation
					INC	INCENTIVE
					EDT401	Design Of Electronic Equipments
					EDT402	CMOS VLSI Design
					EDT403	Digital Signal Processing
					EDT404-3	Digital Communication
9	EDU20W-7009	32942	Bajaj Sejal Ravindra	Div-1	EDP402	CMOS VLSI Design Lab
					EDP403	Digital Signal Processing Lab
					EDP406	Project Phase -1
					EDT405-4	MOOC
					EDP407	FPGA Based Implementation
					INC	INCENTIVE
					EDT401	Design Of Electronic Equipments
					EDT402	CMOS VLSI Design
					EDT403	Digital Signal Processing
					EDT404-3	Digital Communication
10	EDU20W-7010	32959	Jadhav Harsh Ajay	Div-1	EDP402	CMOS VLSI Design Lab
					EDP403	Digital Signal Processing Lab
					EDP406	Project Phase -1
					EDT405-4	MOOC
					EDP407	FPGA Based Implementation
					INC	INCENTIVE
					EDT401	Design Of Electronic Equipments
					EDT402	CMOS VLSI Design
					EDT403	Digital Signal Processing
					EDT404-3	Digital Communication
					EDP402	CMOS VLSI Design Lab
					EDP403	Digital Signal Processing Lab
					EDP406	Project Phase -1
					EDT405-4	MOOC

11	EDU20W-7011	33004	Kulthe Anjali Anil	Div-1	EDP407	FPGA Based Implementation
					INC	INCENTIVE
					EDT401	Design Of Electronic Equipments
					EDT402	CMOS VLSI Design
					EDT403	Digital Signal Processing
					EDT404-3	Digital Communication
12	EDU20W-7012	33080	Shrivastava Ayush Jeetendra	Div-1	EDP402	CMOS VLSI Design Lab
					EDP403	Digital Signal Processing Lab
					EDP406	Project Phase -1
					EDT405-4	MOOC
					EDP407	FPGA Based Implementation
					INC	INCENTIVE
					EDT401	Design Of Electronic Equipments
					EDT402	CMOS VLSI Design
					EDT403	Digital Signal Processing
					EDT404-3	Digital Communication
13	EDU20W-7013	33352	Kushwaha Samarth Anil	Div-1	EDP402	CMOS VLSI Design Lab
					EDP403	Digital Signal Processing Lab
					EDP406	Project Phase -1
					EDT405-4	MOOC
					EDP407	FPGA Based Implementation
					INC	INCENTIVE
					EDT401	Design Of Electronic Equipments
					EDT402	CMOS VLSI Design
					EDT403	Digital Signal Processing
					EDT404-3	Digital Communication
14	EDU20W-7014	33480	Mohit Agrawal	Div-1	EDP402	CMOS VLSI Design Lab
					EDP403	Digital Signal Processing Lab
					EDP406	Project Phase -1
					EDT405-4	MOOC
					EDP407	FPGA Based Implementation
					INC	INCENTIVE
					EDT401	Design Of Electronic Equipments
					EDT402	CMOS VLSI Design
					EDT403	Digital Signal Processing
					EDT404-3	Digital Communication
					EDP402	CMOS VLSI Design Lab

15	EDU20W-7015	33574	Manveen Kaur Oberoi	Div-1	EDP403	Digital Signal Processing Lab
					EDP406	Project Phase -1
					EDT405-4	MOOC
					EDP407	FPGA Based Implementation
					INC	INCENTIVE
					EDT401	Design Of Electronic Equipments
					EDT402	CMOS VLSI Design
					EDT403	Digital Signal Processing
					EDT404-3	Digital Communication
16	EDU20W-7016	33658	Patodia Nandini Uttamkumar	Div-1	EDP402	CMOS VLSI Design Lab
					EDP403	Digital Signal Processing Lab
					EDP406	Project Phase -1
					EDT405-4	MOOC
					EDP407	FPGA Based Implementation
					INC	INCENTIVE
					EDT401	Design Of Electronic Equipments
					EDT402	CMOS VLSI Design
					EDT403	Digital Signal Processing
EDT404-3	Digital Communication					
17	EDU20W-7017	33812	Gothi Anisha Anil	Div-1	EDP402	CMOS VLSI Design Lab
					EDP403	Digital Signal Processing Lab
					EDP406	Project Phase -1
					EDT405-4	MOOC
					EDP407	FPGA Based Implementation
					INC	INCENTIVE
					EDT401	Design Of Electronic Equipments
					EDT402	CMOS VLSI Design
					EDT403	Digital Signal Processing
EDT404-3	Digital Communication					
18	EDU20W-7018	33831	Jaiswar Pruthvi Akash	Div-1	EDP402	CMOS VLSI Design Lab
					EDP403	Digital Signal Processing Lab
					EDP406	Project Phase -1
					EDT405-4	MOOC
					EDP407	FPGA Based Implementation
					INC	INCENTIVE
					EDT401	Design Of Electronic Equipments
					EDT402	CMOS VLSI Design

					EDT403	Digital Signal Processing
					EDT404-3	Digital Communication
19	EDU20W-7019	33888	Gaware Shraddha Sanjay	Div-1	EDP402	CMOS VLSI Design Lab
					EDP403	Digital Signal Processing Lab
					EDP406	Project Phase -1
					EDT405-4	MOOC
					EDP407	FPGA Based Implementation
					INC	INCENTIVE
					EDT401	Design Of Electronic Equipments
					EDT402	CMOS VLSI Design
					EDT403	Digital Signal Processing
					EDT404-3	Digital Communication
					EDP402	CMOS VLSI Design Lab
20	EDU20W-7020	34064	Nagpure Abhishek Ashok	Div-1	EDP403	Digital Signal Processing Lab
					EDP406	Project Phase -1
					EDT405-4	MOOC
					EDP407	FPGA Based Implementation
					INC	INCENTIVE
					EDT401	Design Of Electronic Equipments
					EDT402	CMOS VLSI Design
					EDT403	Digital Signal Processing
					EDT404-3	Digital Communication
					EDP402	CMOS VLSI Design Lab
21	EDU20W-7021	34349	Arekar Aditya Ajay	Div-1	EDP403	Digital Signal Processing Lab
					EDP406	Project Phase -1
					EDT405-4	MOOC
					EDP407	FPGA Based Implementation
					INC	INCENTIVE
					EDT401	Design Of Electronic Equipments
					EDT402	CMOS VLSI Design
					EDT403	Digital Signal Processing
					EDT404-3	Digital Communication
					EDP402	CMOS VLSI Design Lab
22	EDU20W-7022	34398	Ninave Shreya Purushottam	Div-1	EDP403	Digital Signal Processing Lab
					EDP406	Project Phase -1
					EDT405-4	MOOC
					EDP407	FPGA Based Implementation

22	EDU20W-7022	34338	Pinave Shreya Parashottam	Div-1	INC	INCENTIVE
					EDT401	Design Of Electronic Equipments
					EDT402	CMOS VLSI Design
					EDT403	Digital Signal Processing
					EDT404-3	Digital Communication
23	EDU20W-7023	38139	Parate Priyanka Dhanraj	Div-1	EDP402	CMOS VLSI Design Lab
					EDP403	Digital Signal Processing Lab
					EDP406	Project Phase -1
					EDT405-4	MOOC
					EDP407	FPGA Based Implementation
					INC	INCENTIVE
					EDT401	Design Of Electronic Equipments
					EDT402	CMOS VLSI Design
					EDT403	Digital Signal Processing
					EDT404-3	Digital Communication
24	EDU20W-7024	38212	Humne Prajvali Yashwant	Div-1	EDP402	CMOS VLSI Design Lab
					EDP403	Digital Signal Processing Lab
					EDP406	Project Phase -1
					EDT405-4	MOOC
					EDP407	FPGA Based Implementation
					INC	INCENTIVE
					EDT401	Design Of Electronic Equipments
					EDT402	CMOS VLSI Design
					EDT403	Digital Signal Processing
					EDT404-3	Digital Communication
25	EDU20W-7025	38250	Ingole Sakshi Sanjay	Div-1	EDP402	CMOS VLSI Design Lab
					EDP403	Digital Signal Processing Lab
					EDP406	Project Phase -1
					EDT405-4	MOOC
					EDP407	FPGA Based Implementation
					INC	INCENTIVE
					EDT401	Design Of Electronic Equipments
					EDT402	CMOS VLSI Design
					EDT403	Digital Signal Processing
					EDT404-3	Digital Communication
					EDP402	CMOS VLSI Design Lab
					EDP403	Digital Signal Processing Lab

26	EDU20W-7026	38291	Shende Pranjali Sanjay	Div-1	EDP406	Project Phase -1
					EDT405-4	MOOC
					EDP407	FPGA Based Implementation
					INC	INCENTIVE
					EDT401	Design Of Electronic Equipments
					EDT402	CMOS VLSI Design
					EDT403	Digital Signal Processing
					EDT404-3	Digital Communication
27	EDU20W-7027	38311	Boldhane Prajwal Rajendra	Div-1	EDP402	CMOS VLSI Design Lab
					EDP403	Digital Signal Processing Lab
					EDP406	Project Phase -1
					EDT405-4	MOOC
					EDP407	FPGA Based Implementation
					INC	INCENTIVE
					EDT401	Design Of Electronic Equipments
					EDT402	CMOS VLSI Design
28	EDU20W-7028	38341	Chavhan Lokchand Palasram	Div-1	EDP402	CMOS VLSI Design Lab
					EDP403	Digital Signal Processing Lab
					EDP406	Project Phase -1
					EDT405-4	MOOC
					EDP407	FPGA Based Implementation
					INC	INCENTIVE
					EDT401	Design Of Electronic Equipments
					EDT402	CMOS VLSI Design
29	EDU20W-7029	38345	Mahadule Kalyani Purushottam	Div-1	EDP402	CMOS VLSI Design Lab
					EDP403	Digital Signal Processing Lab
					EDP406	Project Phase -1
					EDT405-4	MOOC
					EDP407	FPGA Based Implementation
					INC	INCENTIVE
					EDT401	Design Of Electronic Equipments
					EDT402	CMOS VLSI Design
EDT403	Digital Signal Processing					



30	EDU20W-7030	38364	Kale Namrata Sanjay	Div-1	EDT404-3	Digital Communication
					EDP402	CMOS VLSI Design Lab
					EDP403	Digital Signal Processing Lab
					EDP406	Project Phase -1
					EDT405-4	MOOC
					EDP407	FPGA Based Implementation
					INC	INCENTIVE
					EDT401	Design Of Electronic Equipments
					EDT402	CMOS VLSI Design
					EDT403	Digital Signal Processing
					EDT404-3	Digital Communication