

**Programme Name : RCOEM - B.E.(Electronics and Communication Engineering) - Electronics and Communication (ECU)**  
**Exam Name : END SEMESTER REGULAR EXAMINATION WINTER 2020 (RW 2020) ( B.E.(Electronics and Communication Engineering) )**

**Syllabus Pattern : 2015**

**Classes / Semester : VII**

**Report Genrate Date : Nov 20, 2020**

Sr. No.	Seat No.	Student Unique No.	Student Name	Batch	Course Code	Course Name
1	ECU20W-7001	2444	Sawal Krishna Brijmohan	Div-2	ECP403	Digital Signal Processing Lab
					ECP404	Digital System Design with HDL Lab
					ECP406	Project Phase - I
					INC	INCENTIVE
					ECT401	Digital Communication
					ECT402	Computer Communication Networks
					ECT403	Digital Signal Processing
					ECT404	Digital System Design with HDL
					ECT405-3	Advanced Microcontrollers
2	ECU20W-7002	8229	Agrawal Ajay Ramprakash	Div-1	ECP403	Digital Signal Processing Lab
					ECP404	Digital System Design with HDL Lab
					ECP406	Project Phase - I
					INC	INCENTIVE
					ECT401	Digital Communication
					ECT402	Computer Communication Networks
					ECT403	Digital Signal Processing
					ECT404	Digital System Design with HDL
					ECT405-3	Advanced Microcontrollers
3	ECU20W-7003	11336	Durgesh Kumar Mishra	Div-1	ECP403	Digital Signal Processing Lab
					ECP404	Digital System Design with HDL Lab
					ECT401	Digital Communication
					ECT402	Computer Communication Networks
					ECT403	Digital Signal Processing
					ECT404	Digital System Design with HDL

4	ECU20W-7004	12578	Gupta Shubham Govind	Div-2	ECT405-3	Advanced Microcontrollers
					ECP403	Digital Signal Processing Lab
					ECP404	Digital System Design with HDL Lab
					ECP406	Project Phase - I
					INC	INCENTIVE
					ECT401	Digital Communication
					ECT402	Computer Communication Networks
					ECT403	Digital Signal Processing
					ECT404	Digital System Design with HDL
					ECT405-3	Advanced Microcontrollers
5	ECU20W-7005	13534	Amit Kumar Chaudhary	Div-1	ECP403	Digital Signal Processing Lab
					ECP404	Digital System Design with HDL Lab
					ECT401	Digital Communication
					ECT402	Computer Communication Networks
					ECT403	Digital Signal Processing
					ECT404	Digital System Design with HDL
					ECT405-3	Advanced Microcontrollers
6	ECU20W-7006	14472	Tanushree Kota	Div-2	ECP403	Digital Signal Processing Lab
					ECP404	Digital System Design with HDL Lab
					ECP406	Project Phase - I
					INC	INCENTIVE
					ECT401	Digital Communication
					ECT402	Computer Communication Networks
					ECT403	Digital Signal Processing
					ECT404	Digital System Design with HDL
					ECT405-3	Advanced Microcontrollers
7	ECU20W-7007	14812	Alison Donald Chatragadda	Div-2	ECP403	Digital Signal Processing Lab
					ECP404	Digital System Design with HDL Lab
					ECP406	Project Phase - I
					INC	INCENTIVE
					ECT401	Digital Communication

					ECT402	Computer Communication Networks
					ECT403	Digital Signal Processing
					ECT404	Digital System Design with HDL
8	ECU20W-7008	16768	Abhinav Sudhakar Bathula	Div-2	ECP403	Digital Signal Processing Lab
					ECP404	Digital System Design with HDL Lab
					ECP406	Project Phase - I
					INC	INCENTIVE
					ECT401	Digital Communication
					ECT402	Computer Communication Networks
					ECT403	Digital Signal Processing
					ECT404	Digital System Design with HDL
					ECT405-3	Advanced Microcontrollers
9	ECU20W-7009	17069	Goyal Raj Sanjay		Div-2	ECP403
				ECP404		Digital System Design with HDL Lab
				ECP406		Project Phase - I
				INC		INCENTIVE
				ECT401		Digital Communication
				ECT402		Computer Communication Networks
				ECT403		Digital Signal Processing
				ECT404		Digital System Design with HDL
				ECT405-3		Advanced Microcontrollers
10	ECU20W-7010	18908	Anshul Sanghi	Div-2		ECP403
					ECP404	Digital System Design with HDL Lab
					ECP406	Project Phase - I
					INC	INCENTIVE
					ECT401	Digital Communication
					ECT402	Computer Communication Networks
					ECT403	Digital Signal Processing
					ECT404	Digital System Design with HDL
					ECT405-3	Advanced Microcontrollers
					ECP403	Digital Signal Processing Lab

11	ECU20W-7011	22461	Bhujwa Abhishek Ramdas	Div-1	ECP404	Digital System Design with HDL Lab
					ECP406	Project Phase - I
					INC	INCENTIVE
					ECT401	Digital Communication
					ECT402	Computer Communication Networks
					ECT403	Digital Signal Processing
					ECT404	Digital System Design with HDL
12	ECU20W-7012	22551	Gharde Sparsh Prashant	Div-2	ECP403	Digital Signal Processing Lab
					ECP404	Digital System Design with HDL Lab
					ECP406	Project Phase - I
					INC	INCENTIVE
					ECT401	Digital Communication
					ECT402	Computer Communication Networks
					ECT403	Digital Signal Processing
					ECT404	Digital System Design with HDL
ECT405-3	Advanced Microcontrollers					
13	ECU20W-7013	23292	Sharma Ujjwal Jeetendra	Div-2	ECP403	Digital Signal Processing Lab
					ECP404	Digital System Design with HDL Lab
					ECP406	Project Phase - I
					INC	INCENTIVE
					ECT401	Digital Communication
					ECT402	Computer Communication Networks
					ECT403	Digital Signal Processing
					ECT404	Digital System Design with HDL
ECT405-3	Advanced Microcontrollers					
14	ECU20W-7014	25973	Channe Himanshu Ravindra	Div-2	ECP403	Digital Signal Processing Lab
					ECP404	Digital System Design with HDL Lab
					ECP406	Project Phase - I
					INC	INCENTIVE
					ECT401	Digital Communication
					ECT402	Computer Communication Networks

					ECT403	Digital Signal Processing
					ECT404	Digital System Design with HDL
					ECT405-3	Advanced Microcontrollers
15	ECU20W-7015	26403	Thakare Pranav Sanjay	Div-2	ECP403	Digital Signal Processing Lab
					ECP404	Digital System Design with HDL Lab
					ECP406	Project Phase - I
					INC	INCENTIVE
					ECT401	Digital Communication
					ECT402	Computer Communication Networks
					ECT403	Digital Signal Processing
					ECT404	Digital System Design with HDL
					ECT405-3	Advanced Microcontrollers
16	ECU20W-7016	26724	Bhullar Sakshi Prithipal Singh	Div-2	ECP403	Digital Signal Processing Lab
					ECP404	Digital System Design with HDL Lab
					ECP406	Project Phase - I
					INC	INCENTIVE
					ECT401	Digital Communication
					ECT402	Computer Communication Networks
					ECT403	Digital Signal Processing
					ECT404	Digital System Design with HDL
					ECT405-3	Advanced Microcontrollers
17	ECU20W-7017	27092	Chhawsaria Piyush Gajanan	Div-1	ECP403	Digital Signal Processing Lab
					ECP404	Digital System Design with HDL Lab
					ECP406	Project Phase - I
					INC	INCENTIVE
					ECT401	Digital Communication
					ECT402	Computer Communication Networks
					ECT403	Digital Signal Processing
					ECT404	Digital System Design with HDL
					ECP403	Digital Signal Processing Lab
					ECP404	Digital System Design with HDL Lab

18	ECU20W-7018	27279	Namdeo Aditya Shailesh	Div-2	ECP406	Project Phase - I
					INC	INCENTIVE
					ECT401	Digital Communication
					ECT402	Computer Communication Networks
					ECT403	Digital Signal Processing
					ECT404	Digital System Design with HDL
					ECT405-3	Advanced Microcontrollers
19	ECU20W-7019	27322	Upwanshi Sourabh Khushal	Div-2	ECP403	Digital Signal Processing Lab
					ECP404	Digital System Design with HDL Lab
					ECP406	Project Phase - I
					INC	INCENTIVE
					ECT401	Digital Communication
					ECT402	Computer Communication Networks
					ECT403	Digital Signal Processing
20	ECU20W-7020	27339	Shreya Kasliwal	Div-1	ECP403	Digital Signal Processing Lab
					ECP404	Digital System Design with HDL Lab
					ECP406	Project Phase - I
					INC	INCENTIVE
					ECT401	Digital Communication
					ECT402	Computer Communication Networks
					ECT403	Digital Signal Processing
21	ECU20W-7021	27417	Agrawal Vedant Harish	Div-1	ECP403	Digital Signal Processing Lab
					ECP404	Digital System Design with HDL Lab
					ECP406	Project Phase - I
					INC	INCENTIVE
					ECT401	Digital Communication
					ECT402	Computer Communication Networks

					ECT403	Digital Signal Processing
					ECT404	Digital System Design with HDL
					ECT405-3	Advanced Microcontrollers
22	ECU20W-7022	29233	Gotaphode Ganesh Yogiraj	Div-1	ECP403	Digital Signal Processing Lab
					ECP404	Digital System Design with HDL Lab
					ECP406	Project Phase - I
					INC	INCENTIVE
					ECT401	Digital Communication
					ECT402	Computer Communication Networks
					ECT403	Digital Signal Processing
					ECT404	Digital System Design with HDL
23	ECU20W-7023	29318	Bobade Sanjana Sanjay	Div-1	ECP403	Digital Signal Processing Lab
					ECP404	Digital System Design with HDL Lab
					ECP406	Project Phase - I
					INC	INCENTIVE
					ECT401	Digital Communication
					ECT402	Computer Communication Networks
					ECT403	Digital Signal Processing
					ECT404	Digital System Design with HDL
					ECT405-3	Advanced Microcontrollers
24	ECU20W-7024	29400	Deshpande Sanskruti Ajay	Div-2	ECP403	Digital Signal Processing Lab
					ECP404	Digital System Design with HDL Lab
					ECP406	Project Phase - I
					INC	INCENTIVE
					ECT401	Digital Communication
					ECT402	Computer Communication Networks
					ECT403	Digital Signal Processing
					ECT404	Digital System Design with HDL
					ECT405-3	Advanced Microcontrollers
					ECP403	Digital Signal Processing Lab
					ECP404	Digital System Design with HDL Lab

25	ECU20W-7025	29413	Hatwar Ketki Sanjay	Div-2	ECP406	Project Phase - I
					INC	INCENTIVE
					ECT401	Digital Communication
					ECT402	Computer Communication Networks
					ECT403	Digital Signal Processing
					ECT404	Digital System Design with HDL
					ECT405-3	Advanced Microcontrollers
26	ECU20W-7026	29484	Lahane Aditty Sunil	Div-1	ECP403	Digital Signal Processing Lab
					ECP404	Digital System Design with HDL Lab
					ECP406	Project Phase - I
					INC	INCENTIVE
					ECT401	Digital Communication
					ECT402	Computer Communication Networks
					ECT403	Digital Signal Processing
27	ECU20W-7027	29514	Chandak Shweta Deepak	Div-1	ECP403	Digital Signal Processing Lab
					ECP404	Digital System Design with HDL Lab
					ECP406	Project Phase - I
					INC	INCENTIVE
					ECT401	Digital Communication
					ECT402	Computer Communication Networks
					ECT403	Digital Signal Processing
28	ECU20W-7028	29571	Deoke Aditya Aniruddha	Div-2	ECP403	Digital Signal Processing Lab
					ECP404	Digital System Design with HDL Lab
					ECP406	Project Phase - I
					INC	INCENTIVE
					ECT401	Digital Communication
					ECT402	Computer Communication Networks



					ECT403	Digital Signal Processing
					ECT404	Digital System Design with HDL
					ECT405-3	Advanced Microcontrollers
29	ECU20W-7029	29575	Juware Rohini Ramakant	Div-2	ECP403	Digital Signal Processing Lab
					ECP404	Digital System Design with HDL Lab
					ECP406	Project Phase - I
					INC	INCENTIVE
					ECT401	Digital Communication
					ECT402	Computer Communication Networks
					ECT403	Digital Signal Processing
					ECT404	Digital System Design with HDL
					ECT405-3	Advanced Microcontrollers
30	ECU20W-7030	29629	Yazala Riya Siril Paul	Div-1	ECP403	Digital Signal Processing Lab
					ECP404	Digital System Design with HDL Lab
					ECP406	Project Phase - I
					INC	INCENTIVE
					ECT401	Digital Communication
					ECT402	Computer Communication Networks
					ECT403	Digital Signal Processing
					ECT404	Digital System Design with HDL
					ECT405-3	Advanced Microcontrollers
31	ECU20W-7031	29674	Kanzarkar Mayurima Sanjay	Div-1	ECP403	Digital Signal Processing Lab
					ECP404	Digital System Design with HDL Lab
					ECP406	Project Phase - I
					INC	INCENTIVE
					ECT401	Digital Communication
					ECT402	Computer Communication Networks
					ECT403	Digital Signal Processing
					ECT404	Digital System Design with HDL
					ECT405-3	Advanced Microcontrollers
					ECP403	Digital Signal Processing Lab

32	ECU20W-7032	29688	Labhe Sahil Dhananjay	Div-2	ECP404	Digital System Design with HDL Lab
					ECP406	Project Phase - I
					INC	INCENTIVE
					ECT401	Digital Communication
					ECT402	Computer Communication Networks
					ECT403	Digital Signal Processing
					ECT404	Digital System Design with HDL
					ECT405-3	Advanced Microcontrollers
33	ECU20W-7033	29789	Khamkar Satyam Rajaram	Div-2	ECP403	Digital Signal Processing Lab
					ECP404	Digital System Design with HDL Lab
					ECP406	Project Phase - I
					INC	INCENTIVE
					ECT401	Digital Communication
					ECT402	Computer Communication Networks
					ECT403	Digital Signal Processing
					ECT404	Digital System Design with HDL
ECT405-3	Advanced Microcontrollers					
34	ECU20W-7034	29861	Potle Sumeet Dilip	Div-2	ECP403	Digital Signal Processing Lab
					ECP404	Digital System Design with HDL Lab
					ECP406	Project Phase - I
					INC	INCENTIVE
					ECT401	Digital Communication
					ECT402	Computer Communication Networks
					ECT403	Digital Signal Processing
					ECT404	Digital System Design with HDL
ECT405-3	Advanced Microcontrollers					
35	ECU20W-7035	29863	Sengupta Abira Tarun	Div-2	ECP403	Digital Signal Processing Lab
					ECP404	Digital System Design with HDL Lab
					ECP406	Project Phase - I
					INC	INCENTIVE
					ECT401	Digital Communication

					ECT402	Computer Communication Networks
					ECT403	Digital Signal Processing
					ECT404	Digital System Design with HDL
					ECT405-3	Advanced Microcontrollers
36	ECU20W-7036	29898	Mundle Shantanu Dhananjay	Div-2	ECP403	Digital Signal Processing Lab
					ECP404	Digital System Design with HDL Lab
					ECP406	Project Phase - I
					INC	INCENTIVE
					ECT401	Digital Communication
					ECT402	Computer Communication Networks
					ECT403	Digital Signal Processing
					ECT404	Digital System Design with HDL
					ECT405-3	Advanced Microcontrollers
37	ECU20W-7037	29956	Bais Aditi Pradipsingh	Div-1	ECP403	Digital Signal Processing Lab
					ECP404	Digital System Design with HDL Lab
					ECP406	Project Phase - I
					INC	INCENTIVE
					ECT401	Digital Communication
					ECT402	Computer Communication Networks
					ECT403	Digital Signal Processing
					ECT404	Digital System Design with HDL
					ECT405-3	Advanced Microcontrollers
38	ECU20W-7038	30230	Amborkar Srujani Sunil	Div-1	ECP403	Digital Signal Processing Lab
					ECP404	Digital System Design with HDL Lab
					ECP406	Project Phase - I
					INC	INCENTIVE
					ECT401	Digital Communication
					ECT402	Computer Communication Networks
					ECT403	Digital Signal Processing
					ECT404	Digital System Design with HDL
					ECT405-3	Advanced Microcontrollers

39	ECU20W-7039	30266	Gidwani Mohit Chandrakumar	Div-2	ECP403	Digital Signal Processing Lab
					ECP404	Digital System Design with HDL Lab
					ECP406	Project Phase - I
					INC	INCENTIVE
					ECT401	Digital Communication
					ECT402	Computer Communication Networks
					ECT403	Digital Signal Processing
					ECT404	Digital System Design with HDL
					ECT405-3	Advanced Microcontrollers
40	ECU20W-7040	30293	Sainani Radhika Harish	Div-1	ECP403	Digital Signal Processing Lab
					ECP404	Digital System Design with HDL Lab
					ECP406	Project Phase - I
					INC	INCENTIVE
					ECT401	Digital Communication
					ECT402	Computer Communication Networks
					ECT403	Digital Signal Processing
					ECT404	Digital System Design with HDL
					ECT405-3	Advanced Microcontrollers
41	ECU20W-7041	30383	Shrievs Rohit Desmond	Div-2	ECP403	Digital Signal Processing Lab
					ECP404	Digital System Design with HDL Lab
					ECP406	Project Phase - I
					INC	INCENTIVE
					ECT401	Digital Communication
					ECT402	Computer Communication Networks
					ECT403	Digital Signal Processing
					ECT404	Digital System Design with HDL
					ECT405-3	Advanced Microcontrollers
					ECP403	Digital Signal Processing Lab
					ECP404	Digital System Design with HDL Lab
					ECP406	Project Phase - I
					INC	INCENTIVE

42	ECU20W-7042	30399	Pittule Bhargavi Aniruddha	Div-2	ECT401	Digital Communication
					ECT402	Computer Communication Networks
					ECT403	Digital Signal Processing
					ECT404	Digital System Design with HDL
					ECT405-3	Advanced Microcontrollers
43	ECU20W-7043	30462	Vignesh Hariharan	Div-1	ECP403	Digital Signal Processing Lab
					ECP404	Digital System Design with HDL Lab
					ECP406	Project Phase - I
					INC	INCENTIVE
					ECT401	Digital Communication
					ECT402	Computer Communication Networks
					ECT403	Digital Signal Processing
ECT404	Digital System Design with HDL					
44	ECU201W-7044	30606	Uddanwadiker Veerashri Vikram	Div-1	ECP403	Digital Signal Processing Lab
					ECP404	Digital System Design with HDL Lab
					ECP406	Project Phase - I
					INC	INCENTIVE
					ECT401	Digital Communication
					ECT402	Computer Communication Networks
					ECT403	Digital Signal Processing
					ECT404	Digital System Design with HDL
					ECT405-3	Advanced Microcontrollers
45	ECU201W-7045	30627	Shruti Kela	Div-2	ECP403	Digital Signal Processing Lab
					ECP404	Digital System Design with HDL Lab
					ECP406	Project Phase - I
					INC	INCENTIVE
					ECT401	Digital Communication
					ECT402	Computer Communication Networks
					ECT403	Digital Signal Processing
					ECT404	Digital System Design with HDL
					ECT405-3	Advanced Microcontrollers

46	ECU201W-7046	30703	Gumma Manasa	Div-1	ECP403	Digital Signal Processing Lab
					ECP404	Digital System Design with HDL Lab
					ECP406	Project Phase - I
					INC	INCENTIVE
					ECT401	Digital Communication
					ECT402	Computer Communication Networks
					ECT403	Digital Signal Processing
					ECT404	Digital System Design with HDL
47	ECU20W-7047	30749	Amit Kumar Tiwari	Div-2	ECP403	Digital Signal Processing Lab
					ECP404	Digital System Design with HDL Lab
					ECP406	Project Phase - I
					INC	INCENTIVE
					ECT401	Digital Communication
					ECT402	Computer Communication Networks
					ECT403	Digital Signal Processing
					ECT404	Digital System Design with HDL
ECT405-3	Advanced Microcontrollers					
48	ECU20W-7048	30763	Ghonge Monish Ashok	Div-2	ECP403	Digital Signal Processing Lab
					ECP404	Digital System Design with HDL Lab
					ECP406	Project Phase - I
					INC	INCENTIVE
					ECT401	Digital Communication
					ECT402	Computer Communication Networks
					ECT403	Digital Signal Processing
					ECT404	Digital System Design with HDL
ECT405-3	Advanced Microcontrollers					
49	ECU20W-7049	30797	Barapatre Rohit Raju	Div-2	ECP403	Digital Signal Processing Lab
					ECP404	Digital System Design with HDL Lab
					ECP406	Project Phase - I
					INC	INCENTIVE
					ECT401	Digital Communication

					ECT402	Computer Communication Networks
					ECT403	Digital Signal Processing
					ECT404	Digital System Design with HDL
					ECT405-3	Advanced Microcontrollers
50	ECU20W-7050	30921	Rahul Kumar Sharma	Div-2	ECP403	Digital Signal Processing Lab
					ECP404	Digital System Design with HDL Lab
					ECP406	Project Phase - I
					INC	INCENTIVE
					ECT401	Digital Communication
					ECT402	Computer Communication Networks
					ECT403	Digital Signal Processing
					ECT404	Digital System Design with HDL
					ECT405-3	Advanced Microcontrollers
51	ECU20W-7051	30987	Ashita Upadhyay	Div-1	ECP403	Digital Signal Processing Lab
					ECP404	Digital System Design with HDL Lab
					ECP406	Project Phase - I
					INC	INCENTIVE
					ECT401	Digital Communication
					ECT402	Computer Communication Networks
					ECT403	Digital Signal Processing
					ECT404	Digital System Design with HDL
					ECT405-3	Advanced Microcontrollers
52	ECU20W-7052	31448	Kulkarni Atharva Abhay	Div-2	ECP403	Digital Signal Processing Lab
					ECP404	Digital System Design with HDL Lab
					ECP406	Project Phase - I
					INC	INCENTIVE
					ECT401	Digital Communication
					ECT402	Computer Communication Networks
					ECT403	Digital Signal Processing
					ECT404	Digital System Design with HDL
					ECT405-3	Advanced Microcontrollers

53	ECU20W-7053	31508	Tahiliani Harsh Manoj	Div-2	ECP403	Digital Signal Processing Lab
					ECP404	Digital System Design with HDL Lab
					ECP406	Project Phase - I
					INC	INCENTIVE
					ECT401	Digital Communication
					ECT402	Computer Communication Networks
					ECT403	Digital Signal Processing
					ECT404	Digital System Design with HDL
					ECT405-3	Advanced Microcontrollers
54	ECU20W-7054	31534	Anshuman Ray	Div-1	ECP403	Digital Signal Processing Lab
					ECP404	Digital System Design with HDL Lab
					ECP406	Project Phase - I
					INC	INCENTIVE
					ECT401	Digital Communication
					ECT402	Computer Communication Networks
					ECT403	Digital Signal Processing
					ECT404	Digital System Design with HDL
					ECT405-3	Advanced Microcontrollers
55	ECU20W-7055	31654	Soni Yash Manoj	Div-1	ECP403	Digital Signal Processing Lab
					ECP404	Digital System Design with HDL Lab
					ECP406	Project Phase - I
					INC	INCENTIVE
					ECT401	Digital Communication
					ECT402	Computer Communication Networks
					ECT403	Digital Signal Processing
					ECT404	Digital System Design with HDL
					ECT405-3	Advanced Microcontrollers
					ECP403	Digital Signal Processing Lab
					ECP404	Digital System Design with HDL Lab
					ECP406	Project Phase - I
					INC	INCENTIVE



56	ECU20W-7056	31778	Kahu Atharva Shripad	Div-2	ECT401	Digital Communication
					ECT402	Computer Communication Networks
					ECT403	Digital Signal Processing
					ECT404	Digital System Design with HDL
					ECT405-3	Advanced Microcontrollers
57	ECU20W-7057	31954	Kohad Shreya Dnyaneshwar	Div-1	ECP403	Digital Signal Processing Lab
					ECP404	Digital System Design with HDL Lab
					ECP406	Project Phase - I
					INC	INCENTIVE
					ECT401	Digital Communication
					ECT402	Computer Communication Networks
					ECT403	Digital Signal Processing
					ECT404	Digital System Design with HDL
ECT405-3	Advanced Microcontrollers					
58	ECU20W-7058	32177	Jain Prerana Vineet	Div-1	ECP403	Digital Signal Processing Lab
					ECP404	Digital System Design with HDL Lab
					ECP406	Project Phase - I
					INC	INCENTIVE
					ECT401	Digital Communication
					ECT402	Computer Communication Networks
					ECT403	Digital Signal Processing
					ECT404	Digital System Design with HDL
ECT405-3	Advanced Microcontrollers					
59	ECU20W-7059	32233	Raut Ujwal Pramod	Div-2	ECP403	Digital Signal Processing Lab
					ECP404	Digital System Design with HDL Lab
					ECP406	Project Phase - I
					INC	INCENTIVE
					ECT401	Digital Communication
					ECT402	Computer Communication Networks
					ECT403	Digital Signal Processing
					ECT404	Digital System Design with HDL

					ECT405-3	Advanced Microcontrollers
60	ECU20W-7060	32274	Gupta Devanshi Vijay Kumar	Div-1	ECP403	Digital Signal Processing Lab
					ECP404	Digital System Design with HDL Lab
					ECP406	Project Phase - I
					INC	INCENTIVE
					ECT401	Digital Communication
					ECT402	Computer Communication Networks
					ECT403	Digital Signal Processing
					ECT404	Digital System Design with HDL
					ECT405-3	Advanced Microcontrollers
61	ECU20W-7061	32304	Sheikh Faizan Mushtaque	Div-2	ECP403	Digital Signal Processing Lab
					ECP404	Digital System Design with HDL Lab
					ECP406	Project Phase - I
					INC	INCENTIVE
					ECT401	Digital Communication
					ECT402	Computer Communication Networks
					ECT403	Digital Signal Processing
					ECT404	Digital System Design with HDL
					ECT405-3	Advanced Microcontrollers
62	ECU20W-7062	32348	Bhandari Anand Manoj	Div-2	ECP403	Digital Signal Processing Lab
					ECP404	Digital System Design with HDL Lab
					ECP406	Project Phase - I
					INC	INCENTIVE
					ECT401	Digital Communication
					ECT402	Computer Communication Networks
					ECT403	Digital Signal Processing
					ECT404	Digital System Design with HDL
					ECT405-3	Advanced Microcontrollers
					ECP403	Digital Signal Processing Lab
					ECP404	Digital System Design with HDL Lab
					ECP406	Project Phase - I

63	ECU20W-7063	32449	Ritik Yogesh Sarda	Div-1	INC	INCENTIVE
					ECT401	Digital Communication
					ECT402	Computer Communication Networks
					ECT403	Digital Signal Processing
					ECT404	Digital System Design with HDL
ECT405-3	Advanced Microcontrollers					
64	ECU20W-7064	32673	Syed Arbaaz Khurram	Div-1	ECP403	Digital Signal Processing Lab
					ECP404	Digital System Design with HDL Lab
					ECP406	Project Phase - I
					INC	INCENTIVE
					ECT401	Digital Communication
					ECT402	Computer Communication Networks
					ECT403	Digital Signal Processing
ECT404	Digital System Design with HDL					
65	ECU20W-7065	32939	Baldawa Sakshi Sanjay	Div-2	ECP403	Digital Signal Processing Lab
					ECP404	Digital System Design with HDL Lab
					ECP406	Project Phase - I
					INC	INCENTIVE
					ECT401	Digital Communication
					ECT402	Computer Communication Networks
					ECT403	Digital Signal Processing
ECT404	Digital System Design with HDL					
66	ECU20W-7066	32949	Pimpley Ketki Raju	Div-1	ECP403	Digital Signal Processing Lab
					ECP404	Digital System Design with HDL Lab
					ECP406	Project Phase - I
					INC	INCENTIVE
					ECT401	Digital Communication
					ECT402	Computer Communication Networks
					ECT403	Digital Signal Processing

					ECT404	Digital System Design with HDL
					ECT405-3	Advanced Microcontrollers
67	ECU20W-7067	32956	Chordiya Yogesh Navin	Div-2	ECP403	Digital Signal Processing Lab
					ECP404	Digital System Design with HDL Lab
					ECP406	Project Phase - I
					INC	INCENTIVE
					ECT401	Digital Communication
					ECT402	Computer Communication Networks
					ECT403	Digital Signal Processing
					ECT404	Digital System Design with HDL
					ECT405-3	Advanced Microcontrollers
68	ECU20W-7068	32957	Mundhada Prasanna Ashok	Div-2	ECP403	Digital Signal Processing Lab
					ECP404	Digital System Design with HDL Lab
					ECP406	Project Phase - I
					INC	INCENTIVE
					ECT401	Digital Communication
					ECT402	Computer Communication Networks
					ECT403	Digital Signal Processing
					ECT404	Digital System Design with HDL
					ECT405-3	Advanced Microcontrollers
69	ECU20W-7069	32986	Jha Abhijeet Santosh	Div-2	ECP403	Digital Signal Processing Lab
					ECP404	Digital System Design with HDL Lab
					ECP406	Project Phase - I
					INC	INCENTIVE
					ECT401	Digital Communication
					ECT402	Computer Communication Networks
					ECT403	Digital Signal Processing
					ECT404	Digital System Design with HDL
					ECT405-3	Advanced Microcontrollers
					ECP403	Digital Signal Processing Lab
					ECP404	Digital System Design with HDL Lab

70	ECU20W-7070	32994	Singh Aryavrat Dhirendrapratap	Div-1	ECP406	Project Phase - I
					INC	INCENTIVE
					ECT401	Digital Communication
					ECT402	Computer Communication Networks
					ECT403	Digital Signal Processing
					ECT404	Digital System Design with HDL
					ECT405-3	Advanced Microcontrollers
71	ECU20W-7071	33005	Parwarkar Shrushti Dilip	Div-1	ECP403	Digital Signal Processing Lab
					ECP404	Digital System Design with HDL Lab
					ECP406	Project Phase - I
					INC	INCENTIVE
					ECT401	Digital Communication
					ECT402	Computer Communication Networks
					ECT403	Digital Signal Processing
72	ECU20W-7072	33027	Agrawal Amit Sanjay	Div-2	ECP403	Digital Signal Processing Lab
					ECP404	Digital System Design with HDL Lab
					ECP406	Project Phase - I
					INC	INCENTIVE
					ECT401	Digital Communication
					ECT402	Computer Communication Networks
					ECT403	Digital Signal Processing
73	ECU20W-7073	33049	Kumar Rishikesh D	Div-2	ECP403	Digital Signal Processing Lab
					ECP404	Digital System Design with HDL Lab
					ECP406	Project Phase - I
					INC	INCENTIVE
					ECT401	Digital Communication
					ECT402	Computer Communication Networks

					ECT403	Digital Signal Processing
					ECT404	Digital System Design with HDL
					ECT405-3	Advanced Microcontrollers
74	ECU20W-7074	33057	Srivastava Abheek Sunilkumar	Div-1	ECP403	Digital Signal Processing Lab
					ECP404	Digital System Design with HDL Lab
					ECP406	Project Phase - I
					INC	INCENTIVE
					ECT401	Digital Communication
					ECT402	Computer Communication Networks
					ECT403	Digital Signal Processing
					ECT404	Digital System Design with HDL
					ECT405-3	Advanced Microcontrollers
75	ECU20W-7075	33074	Trivedi Lokesh Jawahar	Div-2	ECP403	Digital Signal Processing Lab
					ECP404	Digital System Design with HDL Lab
					ECP406	Project Phase - I
					INC	INCENTIVE
					ECT401	Digital Communication
					ECT402	Computer Communication Networks
					ECT403	Digital Signal Processing
					ECT404	Digital System Design with HDL
					ECT405-3	Advanced Microcontrollers
76	ECU20W-7076	33085	Arora Chinmay Pradeep	Div-1	ECP403	Digital Signal Processing Lab
					ECP404	Digital System Design with HDL Lab
					ECP406	Project Phase - I
					INC	INCENTIVE
					ECT401	Digital Communication
					ECT402	Computer Communication Networks
					ECT403	Digital Signal Processing
					ECT404	Digital System Design with HDL
					ECT405-3	Advanced Microcontrollers
					ECP403	Digital Signal Processing Lab

77	ECU20W-7077	33087	Dimple Agrawal	Div-2	ECP404	Digital System Design with HDL Lab
					ECP406	Project Phase - I
					INC	INCENTIVE
					ECT401	Digital Communication
					ECT402	Computer Communication Networks
					ECT403	Digital Signal Processing
					ECT404	Digital System Design with HDL
					ECT405-3	Advanced Microcontrollers
78	ECU20W-7078	33092	Wankhede Srujan Nitin	Div-1	ECP403	Digital Signal Processing Lab
					ECP404	Digital System Design with HDL Lab
					ECP406	Project Phase - I
					INC	INCENTIVE
					ECT401	Digital Communication
					ECT402	Computer Communication Networks
					ECT403	Digital Signal Processing
					ECT404	Digital System Design with HDL
ECT405-3	Advanced Microcontrollers					
79	ECU20W-7079	33112	Rastogi Soumya Praveen	Div-1	ECP403	Digital Signal Processing Lab
					ECP404	Digital System Design with HDL Lab
					ECP406	Project Phase - I
					INC	INCENTIVE
					ECT401	Digital Communication
					ECT402	Computer Communication Networks
					ECT403	Digital Signal Processing
					ECT404	Digital System Design with HDL
ECT405-3	Advanced Microcontrollers					
80	ECU20W-7080	33122	Tiwari Sanjana Dharmendra	Div-1	ECP403	Digital Signal Processing Lab
					ECP404	Digital System Design with HDL Lab
					ECP406	Project Phase - I
					INC	INCENTIVE
					ECT401	Digital Communication

					ECT402	Computer Communication Networks
					ECT403	Digital Signal Processing
					ECT404	Digital System Design with HDL
					ECT405-3	Advanced Microcontrollers
81	ECU20W-7081	33189	Pranit Sai Dagguapti	Div-2	ECP403	Digital Signal Processing Lab
					ECP404	Digital System Design with HDL Lab
					ECP406	Project Phase - I
					INC	INCENTIVE
					ECT401	Digital Communication
					ECT402	Computer Communication Networks
					ECT403	Digital Signal Processing
					ECT404	Digital System Design with HDL
					ECT405-3	Advanced Microcontrollers
82	ECU20W-7082	33203	Mali Charu Chandrakant	Div-1	ECP403	Digital Signal Processing Lab
					ECP404	Digital System Design with HDL Lab
					ECP406	Project Phase - I
					INC	INCENTIVE
					ECT401	Digital Communication
					ECT402	Computer Communication Networks
					ECT403	Digital Signal Processing
					ECT404	Digital System Design with HDL
					ECT405-3	Advanced Microcontrollers
83	ECU20W-7083	33209	Maheshwari Adivardhan Akash	Div-2	ECP403	Digital Signal Processing Lab
					ECP404	Digital System Design with HDL Lab
					ECP406	Project Phase - I
					INC	INCENTIVE
					ECT401	Digital Communication
					ECT402	Computer Communication Networks
					ECT403	Digital Signal Processing
					ECT404	Digital System Design with HDL
					ECT405-3	Advanced Microcontrollers



84	ECU20W-7084	33227	Singh Shubham Ramesh	Div-1	ECP403	Digital Signal Processing Lab
					ECP404	Digital System Design with HDL Lab
					ECP406	Project Phase - I
					INC	INCENTIVE
					ECT401	Digital Communication
					ECT402	Computer Communication Networks
					ECT403	Digital Signal Processing
					ECT404	Digital System Design with HDL
85	ECU20W-7085	33229	Pethkar Hrishikesh Prashant	Div-1	ECP403	Digital Signal Processing Lab
					ECP404	Digital System Design with HDL Lab
					ECP406	Project Phase - I
					INC	INCENTIVE
					ECT401	Digital Communication
					ECT402	Computer Communication Networks
					ECT403	Digital Signal Processing
					ECT404	Digital System Design with HDL
ECT405-3	Advanced Microcontrollers					
86	ECU20W-7086	33235	Khandelwal Krutika Ravindra	Div-1	ECP403	Digital Signal Processing Lab
					ECP404	Digital System Design with HDL Lab
					ECP406	Project Phase - I
					INC	INCENTIVE
					ECT401	Digital Communication
					ECT402	Computer Communication Networks
					ECT403	Digital Signal Processing
					ECT404	Digital System Design with HDL
ECT405-3	Advanced Microcontrollers					
87	ECU20W-7087	33244	Rathod Sakshi Manish	Div-2	ECP403	Digital Signal Processing Lab
					ECP404	Digital System Design with HDL Lab
					ECP406	Project Phase - I
					INC	INCENTIVE
					ECT401	Digital Communication

					ECT402	Computer Communication Networks
					ECT403	Digital Signal Processing
					ECT404	Digital System Design with HDL
					ECT405-3	Advanced Microcontrollers
88	ECU20W-7088	33269	Bhartiya Ketan Shyam	Div-1	ECP403	Digital Signal Processing Lab
					ECP404	Digital System Design with HDL Lab
					ECP406	Project Phase - I
					INC	INCENTIVE
					ECT401	Digital Communication
					ECT402	Computer Communication Networks
					ECT403	Digital Signal Processing
					ECT404	Digital System Design with HDL
					ECT405-3	Advanced Microcontrollers
89	ECU20W-7089	33280	Hurkat Harsh Jagdish	Div-1	ECP403	Digital Signal Processing Lab
					ECP404	Digital System Design with HDL Lab
					ECP406	Project Phase - I
					INC	INCENTIVE
					ECT401	Digital Communication
					ECT402	Computer Communication Networks
					ECT403	Digital Signal Processing
					ECT404	Digital System Design with HDL
					ECT405-3	Advanced Microcontrollers
90	ECU20W-7090	33291	Pathak Prathamesh Vivek	Div-2	ECP403	Digital Signal Processing Lab
					ECP404	Digital System Design with HDL Lab
					ECP406	Project Phase - I
					INC	INCENTIVE
					ECT401	Digital Communication
					ECT402	Computer Communication Networks
					ECT403	Digital Signal Processing
					ECT404	Digital System Design with HDL
					ECT405-3	Advanced Microcontrollers

91	ECU20W-7091	33299	Neeha Ali	Div-2	ECP403	Digital Signal Processing Lab
					ECP404	Digital System Design with HDL Lab
					ECP406	Project Phase - I
					INC	INCENTIVE
					ECT401	Digital Communication
					ECT402	Computer Communication Networks
					ECT403	Digital Signal Processing
					ECT404	Digital System Design with HDL
					ECT405-3	Advanced Microcontrollers
92	ECU20W-7092	33314	Agrawal Vedant Susheel	Div-1	ECP403	Digital Signal Processing Lab
					ECP404	Digital System Design with HDL Lab
					ECP406	Project Phase - I
					INC	INCENTIVE
					ECT401	Digital Communication
					ECT402	Computer Communication Networks
					ECT403	Digital Signal Processing
					ECT404	Digital System Design with HDL
					ECT405-3	Advanced Microcontrollers
93	ECU20W-7093	33315	Samant Akshita Tarun	Div-1	ECP403	Digital Signal Processing Lab
					ECP404	Digital System Design with HDL Lab
					ECP406	Project Phase - I
					INC	INCENTIVE
					ECT401	Digital Communication
					ECT402	Computer Communication Networks
					ECT403	Digital Signal Processing
					ECT404	Digital System Design with HDL
					ECT405-3	Advanced Microcontrollers
					ECP403	Digital Signal Processing Lab
					ECP404	Digital System Design with HDL Lab
					ECP406	Project Phase - I
					INC	INCENTIVE

94	ECU20W-7094	33335	Rajput Tinal Vijay	Div-2	ECT401	Digital Communication
					ECT402	Computer Communication Networks
					ECT403	Digital Signal Processing
					ECT404	Digital System Design with HDL
					ECT405-3	Advanced Microcontrollers
95	ECU20W-7095	33339	Dhruv Himanshu Mahesh	Div-1	ECP403	Digital Signal Processing Lab
					ECP404	Digital System Design with HDL Lab
					ECP406	Project Phase - I
					INC	INCENTIVE
					ECT401	Digital Communication
					ECT402	Computer Communication Networks
					ECT403	Digital Signal Processing
					ECT404	Digital System Design with HDL
ECT405-3	Advanced Microcontrollers					
96	ECU20W-7096	33345	Naseem Sana Kuresh	Div-2	ECP403	Digital Signal Processing Lab
					ECP404	Digital System Design with HDL Lab
					ECP406	Project Phase - I
					INC	INCENTIVE
					ECT401	Digital Communication
					ECT402	Computer Communication Networks
					ECT403	Digital Signal Processing
					ECT404	Digital System Design with HDL
ECT405-3	Advanced Microcontrollers					
97	ECU20W-7097	33374	Naidu N Rahul N Kumar	Div-1	ECP403	Digital Signal Processing Lab
					ECP404	Digital System Design with HDL Lab
					ECP406	Project Phase - I
					INC	INCENTIVE
					ECT401	Digital Communication
					ECT402	Computer Communication Networks
					ECT403	Digital Signal Processing
					ECT404	Digital System Design with HDL

					ECT405-3	Advanced Microcontrollers					
98	ECU20W-7098	33378	Kukreti Shubham Roshan	Div-1	ECP403	Digital Signal Processing Lab					
					ECP404	Digital System Design with HDL Lab					
					ECP406	Project Phase - I					
					INC	INCENTIVE					
					ECT401	Digital Communication					
					ECT402	Computer Communication Networks					
					ECT403	Digital Signal Processing					
					ECT404	Digital System Design with HDL					
					ECT405-3	Advanced Microcontrollers					
										ECP403	Digital Signal Processing Lab
99	ECU20W-7099	33384	Jaya Kochar	Div-1	ECP404	Digital System Design with HDL Lab					
					ECP406	Project Phase - I					
					INC	INCENTIVE					
					ECT401	Digital Communication					
					ECT402	Computer Communication Networks					
					ECT403	Digital Signal Processing					
					ECT404	Digital System Design with HDL					
					ECT405-3	Advanced Microcontrollers					
										ECP403	Digital Signal Processing Lab
					100	ECU20W-7100	33385	Khanna Garvit Vinod	Div-1	ECP404	Digital System Design with HDL Lab
ECP406	Project Phase - I										
INC	INCENTIVE										
ECT401	Digital Communication										
ECT402	Computer Communication Networks										
ECT403	Digital Signal Processing										
ECT404	Digital System Design with HDL										
ECT405-3	Advanced Microcontrollers										
										ECP403	Digital Signal Processing Lab
										ECP404	Digital System Design with HDL Lab
					ECP406	Project Phase - I					

101	ECU20W-7101	33387	Trivedi Ayushi Yogesh	Div-2	INC	INCENTIVE
					ECT401	Digital Communication
					ECT402	Computer Communication Networks
					ECT403	Digital Signal Processing
					ECT404	Digital System Design with HDL
					ECT405-3	Advanced Microcontrollers
102	ECU20W-7102	33394	Mandaogade Tanvi Nitin	Div-1	ECP403	Digital Signal Processing Lab
					ECP404	Digital System Design with HDL Lab
					ECP406	Project Phase - I
					INC	INCENTIVE
					ECT401	Digital Communication
					ECT402	Computer Communication Networks
					ECT403	Digital Signal Processing
					ECT404	Digital System Design with HDL
103	ECU20W-7103	33395	Sakshi Atul Ganar	Div-1	ECP403	Digital Signal Processing Lab
					ECP404	Digital System Design with HDL Lab
					ECP406	Project Phase - I
					INC	INCENTIVE
					ECT401	Digital Communication
					ECT402	Computer Communication Networks
					ECT403	Digital Signal Processing
					ECT404	Digital System Design with HDL
104	ECU20W-7104	33396	Choudhary Aman Laxman	Div-1	ECP403	Digital Signal Processing Lab
					ECP404	Digital System Design with HDL Lab
					ECP406	Project Phase - I
					INC	INCENTIVE
					ECT401	Digital Communication
					ECT402	Computer Communication Networks
					ECT403	Digital Signal Processing

					ECT404	Digital System Design with HDL
					ECT405-3	Advanced Microcontrollers
105	ECU20W-7105	33407	Khan Tauqeer Shafee Mohammed	Div-2	ECP403	Digital Signal Processing Lab
					ECP404	Digital System Design with HDL Lab
					ECP406	Project Phase - I
					INC	INCENTIVE
					ECT401	Digital Communication
					ECT402	Computer Communication Networks
					ECT403	Digital Signal Processing
					ECT404	Digital System Design with HDL
					ECT405-3	Advanced Microcontrollers
106	ECU20W-7106	33463	Srivastava Sanskriti Ashish	Div-1	ECP403	Digital Signal Processing Lab
					ECP404	Digital System Design with HDL Lab
					ECP406	Project Phase - I
					INC	INCENTIVE
					ECT401	Digital Communication
					ECT402	Computer Communication Networks
					ECT403	Digital Signal Processing
					ECT404	Digital System Design with HDL
					ECT405-3	Advanced Microcontrollers
107	ECU20W-7107	33465	Kothari Vedant Sunil	Div-2	ECP403	Digital Signal Processing Lab
					ECP404	Digital System Design with HDL Lab
					ECP406	Project Phase - I
					INC	INCENTIVE
					ECT401	Digital Communication
					ECT402	Computer Communication Networks
					ECT403	Digital Signal Processing
					ECT404	Digital System Design with HDL
					ECT405-3	Advanced Microcontrollers
					ECP403	Digital Signal Processing Lab
					ECP404	Digital System Design with HDL Lab

108	ECU20W-7108	33470	Gupta Riya Pravin	Div-1	ECP406	Project Phase - I
					INC	INCENTIVE
					ECT401	Digital Communication
					ECT402	Computer Communication Networks
					ECT403	Digital Signal Processing
					ECT404	Digital System Design with HDL
					ECT405-3	Advanced Microcontrollers
109	ECU20W-7109	33483	Agrawal Shubham Sanjay	Div-1	ECP403	Digital Signal Processing Lab
					ECP404	Digital System Design with HDL Lab
					ECP406	Project Phase - I
					INC	INCENTIVE
					ECT401	Digital Communication
					ECT402	Computer Communication Networks
					ECT403	Digital Signal Processing
110	ECU20W-7110	33488	Chaturvedi Shashank Sanat	Div-2	ECP403	Digital Signal Processing Lab
					ECP404	Digital System Design with HDL Lab
					ECP406	Project Phase - I
					INC	INCENTIVE
					ECT401	Digital Communication
					ECT402	Computer Communication Networks
					ECT403	Digital Signal Processing
111	ECU20W-7111	33501	Verma Ashish Anil	Div-1	ECP403	Digital Signal Processing Lab
					ECP404	Digital System Design with HDL Lab
					ECP406	Project Phase - I
					INC	INCENTIVE
					ECT401	Digital Communication
					ECT402	Computer Communication Networks



					ECT403	Digital Signal Processing
					ECT404	Digital System Design with HDL
					ECT405-3	Advanced Microcontrollers
112	ECU20W-7112	33516	Deshmukh Sampada Ajay	Div-1	ECP403	Digital Signal Processing Lab
					ECP404	Digital System Design with HDL Lab
					ECP406	Project Phase - I
					INC	INCENTIVE
					ECT401	Digital Communication
					ECT402	Computer Communication Networks
					ECT403	Digital Signal Processing
					ECT404	Digital System Design with HDL
					ECT405-3	Advanced Microcontrollers
113	ECU20W-7113	33524	Rathor Shivani Shrikant	Div-1	ECP403	Digital Signal Processing Lab
					ECP404	Digital System Design with HDL Lab
					ECP406	Project Phase - I
					INC	INCENTIVE
					ECT401	Digital Communication
					ECT402	Computer Communication Networks
					ECT403	Digital Signal Processing
					ECT404	Digital System Design with HDL
					ECT405-3	Advanced Microcontrollers
114	ECU20W-7114	33527	Barapatre Ajinkya Pranay	Div-1	ECP403	Digital Signal Processing Lab
					ECP404	Digital System Design with HDL Lab
					ECP406	Project Phase - I
					INC	INCENTIVE
					ECT401	Digital Communication
					ECT402	Computer Communication Networks
					ECT403	Digital Signal Processing
					ECT404	Digital System Design with HDL
					ECT405-3	Advanced Microcontrollers
					ECP403	Digital Signal Processing Lab

115	ECU20W-7115	33537	Somani Govind Premkumar	Div-1	ECP404	Digital System Design with HDL Lab
					ECP406	Project Phase - I
					INC	INCENTIVE
					ECT401	Digital Communication
					ECT402	Computer Communication Networks
					ECT403	Digital Signal Processing
					ECT404	Digital System Design with HDL
					ECT405-3	Advanced Microcontrollers
116	ECU20W-7116	33556	Mahajan Anant Rajendra	Div-1	ECP403	Digital Signal Processing Lab
					ECP404	Digital System Design with HDL Lab
					ECP406	Project Phase - I
					INC	INCENTIVE
					ECT401	Digital Communication
					ECT402	Computer Communication Networks
					ECT403	Digital Signal Processing
					ECT404	Digital System Design with HDL
ECT405-3	Advanced Microcontrollers					
117	ECU20W-7117	33563	Rathi Pushkar Vinod	Div-2	ECP403	Digital Signal Processing Lab
					ECP404	Digital System Design with HDL Lab
					ECP406	Project Phase - I
					INC	INCENTIVE
					ECT401	Digital Communication
					ECT402	Computer Communication Networks
					ECT403	Digital Signal Processing
					ECT404	Digital System Design with HDL
ECT405-3	Advanced Microcontrollers					
118	ECU20W-7118	33580	Bhartiya Piyush Ravindra	Div-2	ECP403	Digital Signal Processing Lab
					ECP404	Digital System Design with HDL Lab
					ECP406	Project Phase - I
					INC	INCENTIVE
					ECT401	Digital Communication

					ECT402	Computer Communication Networks
					ECT403	Digital Signal Processing
					ECT404	Digital System Design with HDL
					ECT405-3	Advanced Microcontrollers
119	ECU20W-7119	33585	Chaudhari Manas Sadanand	Div-1	ECP403	Digital Signal Processing Lab
					ECP404	Digital System Design with HDL Lab
					ECP406	Project Phase - I
					INC	INCENTIVE
					ECT401	Digital Communication
					ECT402	Computer Communication Networks
					ECT403	Digital Signal Processing
					ECT404	Digital System Design with HDL
					ECT405-3	Advanced Microcontrollers
120	ECU20W-7120	33600	Kulkarni Vedant Chandrashekhar	Div-1	ECP403	Digital Signal Processing Lab
					ECP404	Digital System Design with HDL Lab
					ECP406	Project Phase - I
					INC	INCENTIVE
					ECT401	Digital Communication
					ECT402	Computer Communication Networks
					ECT403	Digital Signal Processing
					ECT404	Digital System Design with HDL
					ECT405-3	Advanced Microcontrollers
121	ECU20W-7121	33602	Jain Yash Sachin	Div-1	ECP403	Digital Signal Processing Lab
					ECP404	Digital System Design with HDL Lab
					ECP406	Project Phase - I
					INC	INCENTIVE
					ECT401	Digital Communication
					ECT402	Computer Communication Networks
					ECT403	Digital Signal Processing
					ECT404	Digital System Design with HDL
					ECT405-3	Advanced Microcontrollers

122	ECU20W-7122	33613	Huddar Rohan Sanjay	Div-1	ECP403	Digital Signal Processing Lab
					ECP404	Digital System Design with HDL Lab
					ECP406	Project Phase - I
					INC	INCENTIVE
					ECT401	Digital Communication
					ECT402	Computer Communication Networks
					ECT403	Digital Signal Processing
					ECT404	Digital System Design with HDL
					ECT405-3	Advanced Microcontrollers
123	ECU20W-7123	33659	Mohane Prachi Manoj	Div-1	ECP403	Digital Signal Processing Lab
					ECP404	Digital System Design with HDL Lab
					ECP406	Project Phase - I
					INC	INCENTIVE
					ECT401	Digital Communication
					ECT402	Computer Communication Networks
					ECT403	Digital Signal Processing
					ECT404	Digital System Design with HDL
					ECT405-3	Advanced Microcontrollers
124	ECU20W-7124	33673	Deshkar Madhura Mukul	Div-1	ECP403	Digital Signal Processing Lab
					ECP404	Digital System Design with HDL Lab
					ECP406	Project Phase - I
					INC	INCENTIVE
					ECT401	Digital Communication
					ECT402	Computer Communication Networks
					ECT403	Digital Signal Processing
					ECT404	Digital System Design with HDL
					ECT405-3	Advanced Microcontrollers
					ECP403	Digital Signal Processing Lab
					ECP404	Digital System Design with HDL Lab
					ECP406	Project Phase - I
					INC	INCENTIVE

125	ECU20W-7125	33677	Kukade Shrushti Dilip	Div-1	ECT401	Digital Communication
					ECT402	Computer Communication Networks
					ECT403	Digital Signal Processing
					ECT404	Digital System Design with HDL
					ECT405-3	Advanced Microcontrollers
126	ECU20W-7126	33678	Agrawal Sumeet Mahesh	Div-2	ECP403	Digital Signal Processing Lab
					ECP404	Digital System Design with HDL Lab
					ECP406	Project Phase - I
					INC	INCENTIVE
					ECT401	Digital Communication
					ECT402	Computer Communication Networks
					ECT403	Digital Signal Processing
					ECT404	Digital System Design with HDL
					ECT405-3	Advanced Microcontrollers
127	ECU20W-7127	33680	Kadoo Sai Sanjay	Div-2	ECP403	Digital Signal Processing Lab
					ECP404	Digital System Design with HDL Lab
					ECP406	Project Phase - I
					INC	INCENTIVE
					ECT401	Digital Communication
					ECT402	Computer Communication Networks
					ECT403	Digital Signal Processing
					ECT404	Digital System Design with HDL
					ECT405-3	Advanced Microcontrollers
128	ECU20W-7128	33734	Bhivgade Himanshu Hariram	Div-2	ECP403	Digital Signal Processing Lab
					ECP404	Digital System Design with HDL Lab
					ECP406	Project Phase - I
					INC	INCENTIVE
					ECT401	Digital Communication
					ECT402	Computer Communication Networks
					ECT403	Digital Signal Processing
					ECT404	Digital System Design with HDL

					ECT405-3	Advanced Microcontrollers
129	ECU20W-7129	33748	Yadav Pooja Devgulam	Div-2	ECP403	Digital Signal Processing Lab
					ECP404	Digital System Design with HDL Lab
					ECP406	Project Phase - I
					INC	INCENTIVE
					ECT401	Digital Communication
					ECT402	Computer Communication Networks
					ECT403	Digital Signal Processing
					ECT404	Digital System Design with HDL
					ECT405-3	Advanced Microcontrollers
130	ECU20W-7130	33749	Wazurkar Prajakta Prakash	Div-2	ECP403	Digital Signal Processing Lab
					ECP404	Digital System Design with HDL Lab
					ECP406	Project Phase - I
					INC	INCENTIVE
					ECT401	Digital Communication
					ECT402	Computer Communication Networks
					ECT403	Digital Signal Processing
					ECT404	Digital System Design with HDL
					ECT405-3	Advanced Microcontrollers
131	ECU20W-7131	33792	Shiwankar Ajit Nevchand	Div-2	ECP403	Digital Signal Processing Lab
					ECP404	Digital System Design with HDL Lab
					ECP406	Project Phase - I
					INC	INCENTIVE
					ECT401	Digital Communication
					ECT402	Computer Communication Networks
					ECT403	Digital Signal Processing
					ECT404	Digital System Design with HDL
					ECT405-3	Advanced Microcontrollers
					ECP403	Digital Signal Processing Lab
					ECP404	Digital System Design with HDL Lab
					ECP406	Project Phase - I

132	ECU20W-7132	33800	Jais Saloni Sanjay	Div-2	INC	INCENTIVE
					ECT401	Digital Communication
					ECT402	Computer Communication Networks
					ECT403	Digital Signal Processing
					ECT404	Digital System Design with HDL
					ECT405-3	Advanced Microcontrollers
133	ECU20W-7133	33824	Jangid Mahesh Jagdish	Div-2	ECP403	Digital Signal Processing Lab
					ECP404	Digital System Design with HDL Lab
					ECP406	Project Phase - I
					INC	INCENTIVE
					ECT401	Digital Communication
					ECT402	Computer Communication Networks
					ECT403	Digital Signal Processing
					ECT404	Digital System Design with HDL
134	ECU20W-7134	34129	Purwar Akshay Narendra	Div-1	ECP403	Digital Signal Processing Lab
					ECP404	Digital System Design with HDL Lab
					ECP406	Project Phase - I
					INC	INCENTIVE
					ECT401	Digital Communication
					ECT402	Computer Communication Networks
					ECT403	Digital Signal Processing
					ECT404	Digital System Design with HDL
135	ECU20W-7135	34182	Shivangi Kakkar	Div-2	ECP403	Digital Signal Processing Lab
					ECP404	Digital System Design with HDL Lab
					ECP406	Project Phase - I
					INC	INCENTIVE
					ECT401	Digital Communication
					ECT402	Computer Communication Networks
					ECT403	Digital Signal Processing

					ECT404	Digital System Design with HDL
					ECT405-3	Advanced Microcontrollers
136	ECU20W-7136	34196	Mankar Shivakshi Sunil	Div-2	ECP403	Digital Signal Processing Lab
					ECP404	Digital System Design with HDL Lab
					ECP406	Project Phase - I
					INC	INCENTIVE
					ECT401	Digital Communication
					ECT402	Computer Communication Networks
					ECT403	Digital Signal Processing
					ECT404	Digital System Design with HDL
					ECT405-3	Advanced Microcontrollers
137	ECU20W-7137	34235	Rathi Mahesh Anil	Div-2	ECP403	Digital Signal Processing Lab
					ECP404	Digital System Design with HDL Lab
					ECP406	Project Phase - I
					INC	INCENTIVE
					ECT401	Digital Communication
					ECT402	Computer Communication Networks
					ECT403	Digital Signal Processing
					ECT404	Digital System Design with HDL
					ECT405-3	Advanced Microcontrollers
138	ECU20W-7138	34277	Jethani Muskan Manojkumar	Div-2	ECP403	Digital Signal Processing Lab
					ECP404	Digital System Design with HDL Lab
					ECP406	Project Phase - I
					INC	INCENTIVE
					ECT401	Digital Communication
					ECT402	Computer Communication Networks
					ECT403	Digital Signal Processing
					ECT404	Digital System Design with HDL
					ECT405-3	Advanced Microcontrollers
					ECP403	Digital Signal Processing Lab
					ECP404	Digital System Design with HDL Lab



139	ECU20W-7139	34332	Mishra Aradhana Dinesh	Div-2	ECP406	Project Phase - I
					INC	INCENTIVE
					ECT401	Digital Communication
					ECT402	Computer Communication Networks
					ECT403	Digital Signal Processing
					ECT404	Digital System Design with HDL
					ECT405-3	Advanced Microcontrollers
140	ECU20W-7140	34404	Rathi Kunjan Gopaldasji	Div-1	ECP403	Digital Signal Processing Lab
					ECP404	Digital System Design with HDL Lab
					ECP406	Project Phase - I
					INC	INCENTIVE
					ECT401	Digital Communication
					ECT402	Computer Communication Networks
					ECT403	Digital Signal Processing
141	ECU20W-7141	38020	Nema Pranjal Ghanshyam	Div-1	ECP403	Digital Signal Processing Lab
					ECP404	Digital System Design with HDL Lab
					ECP406	Project Phase - I
					INC	INCENTIVE
					ECT401	Digital Communication
					ECT402	Computer Communication Networks
					ECT403	Digital Signal Processing
142	ECU20W-7142	38024	Kathoke Riya Jiwan	Div-2	ECP403	Digital Signal Processing Lab
					ECP404	Digital System Design with HDL Lab
					ECP406	Project Phase - I
					INC	INCENTIVE
					ECT401	Digital Communication
					ECT402	Computer Communication Networks

					ECT403	Digital Signal Processing
					ECT404	Digital System Design with HDL
					ECT405-3	Advanced Microcontrollers
143	ECU20W-7143	38050	Nagmote Pratiksha Sharad	Div-2	ECP403	Digital Signal Processing Lab
					ECP404	Digital System Design with HDL Lab
					ECP406	Project Phase - I
					INC	INCENTIVE
					ECT401	Digital Communication
					ECT402	Computer Communication Networks
					ECT403	Digital Signal Processing
					ECT404	Digital System Design with HDL
					ECT405-3	Advanced Microcontrollers
144	ECU20W-7144	38057	Dhapke Ketki Pradeep	Div-1	ECP403	Digital Signal Processing Lab
					ECP404	Digital System Design with HDL Lab
					ECP406	Project Phase - I
					INC	INCENTIVE
					ECT401	Digital Communication
					ECT402	Computer Communication Networks
					ECT403	Digital Signal Processing
					ECT404	Digital System Design with HDL
					ECT405-3	Advanced Microcontrollers
145	ECU20W-7145	38108	Dhamde Shruti Pradeep	Div-1	ECP403	Digital Signal Processing Lab
					ECP404	Digital System Design with HDL Lab
					ECP406	Project Phase - I
					INC	INCENTIVE
					ECT401	Digital Communication
					ECT402	Computer Communication Networks
					ECT403	Digital Signal Processing
					ECT404	Digital System Design with HDL
					ECT405-3	Advanced Microcontrollers
					ECP403	Digital Signal Processing Lab

146	ECU20W-7146	38113	Khivasara Vipul Vijaykumar	Div-2	ECP404	Digital System Design with HDL Lab
					ECP406	Project Phase - I
					INC	INCENTIVE
					ECT401	Digital Communication
					ECT402	Computer Communication Networks
					ECT403	Digital Signal Processing
					ECT404	Digital System Design with HDL
					ECT405-3	Advanced Microcontrollers
147	ECU20W-7147	38121	Virsen Sourabh Sanjay	Div-2	ECP403	Digital Signal Processing Lab
					ECP404	Digital System Design with HDL Lab
					ECP406	Project Phase - I
					INC	INCENTIVE
					ECT401	Digital Communication
					ECT402	Computer Communication Networks
					ECT403	Digital Signal Processing
					ECT404	Digital System Design with HDL
ECT405-3	Advanced Microcontrollers					
148	ECU20W-7148	38122	Mishra Saransh Sudhanshu	Div-1	ECP403	Digital Signal Processing Lab
					ECP404	Digital System Design with HDL Lab
					ECP406	Project Phase - I
					INC	INCENTIVE
					ECT401	Digital Communication
					ECT402	Computer Communication Networks
					ECT403	Digital Signal Processing
					ECT404	Digital System Design with HDL
ECT405-3	Advanced Microcontrollers					
149	ECU20W-7149	38123	Khubchandani Neha Harish	Div-1	ECP403	Digital Signal Processing Lab
					ECP404	Digital System Design with HDL Lab
					ECP406	Project Phase - I
					INC	INCENTIVE
					ECT401	Digital Communication

					ECT402	Computer Communication Networks
					ECT403	Digital Signal Processing
					ECT404	Digital System Design with HDL
					ECT405-3	Advanced Microcontrollers
150	ECU20W-7150	38131	Sharma Koushal Dhiraj	Div-2	ECP403	Digital Signal Processing Lab
					ECP404	Digital System Design with HDL Lab
					ECP406	Project Phase - I
					INC	INCENTIVE
					ECT401	Digital Communication
					ECT402	Computer Communication Networks
					ECT403	Digital Signal Processing
					ECT404	Digital System Design with HDL
					ECT405-3	Advanced Microcontrollers
151	ECU20W-7151	38171	Khire Vedant Pramod	Div-1	ECP403	Digital Signal Processing Lab
					ECP404	Digital System Design with HDL Lab
					ECP406	Project Phase - I
					INC	INCENTIVE
					ECT401	Digital Communication
					ECT402	Computer Communication Networks
					ECT403	Digital Signal Processing
					ECT404	Digital System Design with HDL
					ECT405-3	Advanced Microcontrollers
152	ECU20W-7152	38197	Tiwari Ashish Rajendraprasad	Div-1	ECP403	Digital Signal Processing Lab
					ECP404	Digital System Design with HDL Lab
					ECP406	Project Phase - I
					INC	INCENTIVE
					ECT401	Digital Communication
					ECT402	Computer Communication Networks
					ECT403	Digital Signal Processing
					ECT404	Digital System Design with HDL
					ECT405-3	Advanced Microcontrollers

153	ECU20W-7153	38198	Shukla Shreyansh Santosh	Div-1	ECP403	Digital Signal Processing Lab
					ECP404	Digital System Design with HDL Lab
					ECP406	Project Phase - I
					INC	INCENTIVE
					ECT401	Digital Communication
					ECT402	Computer Communication Networks
					ECT403	Digital Signal Processing
					ECT404	Digital System Design with HDL
					ECT405-3	Advanced Microcontrollers
154	ECU20W-7154	38201	Tule Pranali Rajendra	Div-1	ECP403	Digital Signal Processing Lab
					ECP404	Digital System Design with HDL Lab
					ECP406	Project Phase - I
					INC	INCENTIVE
					ECT401	Digital Communication
					ECT402	Computer Communication Networks
					ECT403	Digital Signal Processing
					ECT404	Digital System Design with HDL
					ECT405-3	Advanced Microcontrollers
155	ECU20W-7155	38204	Iyer Prathamesh Mohan	Div-2	ECP403	Digital Signal Processing Lab
					ECP404	Digital System Design with HDL Lab
					ECP406	Project Phase - I
					INC	INCENTIVE
					ECT401	Digital Communication
					ECT402	Computer Communication Networks
					ECT403	Digital Signal Processing
					ECT404	Digital System Design with HDL
					156	ECU20W-7156
ECP404	Digital System Design with HDL Lab					
ECP406	Project Phase - I					
INC	INCENTIVE					
ECT401	Digital Communication					

					ECT402	Computer Communication Networks
					ECT403	Digital Signal Processing
					ECT404	Digital System Design with HDL
					ECT405-3	Advanced Microcontrollers
157	ECU20W-7157	38343	Pawar Radha Pradeep	Div-2	ECP403	Digital Signal Processing Lab
					ECP404	Digital System Design with HDL Lab
					ECP406	Project Phase - I
					INC	INCENTIVE
					ECT401	Digital Communication
					ECT402	Computer Communication Networks
					ECT403	Digital Signal Processing
					ECT404	Digital System Design with HDL
					ECT405-3	Advanced Microcontrollers
158	ECU20W-7158	38346	Kulkarni Mayuri Raghvendra	Div-2	ECP403	Digital Signal Processing Lab
					ECP404	Digital System Design with HDL Lab
					ECP406	Project Phase - I
					INC	INCENTIVE
					ECT401	Digital Communication
					ECT402	Computer Communication Networks
					ECT403	Digital Signal Processing
					ECT404	Digital System Design with HDL
					ECT405-3	Advanced Microcontrollers
159	ECU20W-7159	38354	Nikumbh Karansingh Darbarsingh	Div-2	ECP403	Digital Signal Processing Lab
					ECP404	Digital System Design with HDL Lab
					ECP406	Project Phase - I
					INC	INCENTIVE
					ECT401	Digital Communication
					ECT402	Computer Communication Networks
					ECT403	Digital Signal Processing
					ECT404	Digital System Design with HDL
					ECT405-3	Advanced Microcontrollers

160	ECU20W-7160	38360	Baghel Manish Rajaram	Div-2	ECP403	Digital Signal Processing Lab
					ECP404	Digital System Design with HDL Lab
					ECP406	Project Phase - I
					INC	INCENTIVE
					ECT401	Digital Communication
					ECT402	Computer Communication Networks
					ECT403	Digital Signal Processing
					ECT404	Digital System Design with HDL
					ECT405-3	Advanced Microcontrollers
161	ECU20W-7161	38388	Mayuri Manoj Shriwaskar	Div-2	ECP403	Digital Signal Processing Lab
					ECP404	Digital System Design with HDL Lab
					ECP406	Project Phase - I
					INC	INCENTIVE
					ECT401	Digital Communication
					ECT402	Computer Communication Networks
					ECT403	Digital Signal Processing
					ECT404	Digital System Design with HDL
					ECT405-3	Advanced Microcontrollers
162	ECU20W-7162	38392	Gupta Shivanshu Rajesh	Div-1	ECP403	Digital Signal Processing Lab
					ECP404	Digital System Design with HDL Lab
					ECP406	Project Phase - I
					INC	INCENTIVE
					ECT401	Digital Communication
					ECT402	Computer Communication Networks
					ECT403	Digital Signal Processing
					ECT404	Digital System Design with HDL
					ECT405-3	Advanced Microcontrollers